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Date: September 29, 2003

Docket No.: 4667-0104P

03916 U.S. PTO 10/671771

MS PATENT APPLICATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s): CHENG, S.J.; LIU, An-Hong; TSENG, Yuan-Ping;

LEE, Y.J.

For: METHOD FOR FABRICATING WAFER-LEVEL CHIP SCALE PACKAGES

Enclosed are:

A specification consisting of Eight (8) pages

∑ Three (3) sheet(s) formal drawings

 $oxed{\boxtimes}$ An assignment of the invention

☐ Applicant claims small entity status under 37 C.F.R. § 1.27

Applicant does not claim priority

Applicant claims the right of priority under 35 U.S.C. § 119 based on Application No(s). filed in on . Certified copy(ies) is(are) attached hereto. Certified copy(ies) will follow.
Executed Declaration (Original Photocopy)
Application Data Sheet in accordance with 37 C.F.R. § 1.76
Preliminary Amendment
Information Disclosure Statement, PTO-1449 and reference(s)
Other:
Applicant requests early publication - \$300.00 publication fee
Non-publication Request and Certification under 35 U.S.C. § 122(b)(2)(B)(i)

The filing fee has been calculated as shown below:

			LARGE ENTITY	SMALL ENTITY		
	BASIC	FEE	\$750.00	\$375.00		
	NUMBER FILED		RATE FEE	RATE FEE		
TOTAL	7- 20 =	0	X 18 = \$0.00	x 9 = \$0.00		
INDEPENDENT CLAIMS	1- 3 =	. 0	x 84 = \$0.00	x 42 = \$0.00		
	LE DEPENDEN PRESENTED	T	+ \$280.00	+ \$140.00		
		TOTAL	\$750.00	\$0.00		

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If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

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Attachment(s)

(Rev. 08/21/03)